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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10043104	FILING DATE 01/14/2002	CLASS 438-25	SUBCLASS 7	GAU 2823	EXAMINER 2827 Tol. bryt
<b>**APPLICANTS:</b> Corisis David;					
<b>**CONTINUING DATA VERIFIED:</b> THIS APPLICATION IS A CON OF 09/731,803 12/08/2000 WHICH IS A DIV OF 09/191,037 11/12/1998 PAT 6,184,465					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> <i>Seven had parent</i>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO M4065.0317/P317-B	
TITLE : Semiconductor package				U.S. DEPT. OF COMM. PAT. & TM-PTO-436L (Rev. 12-94)	

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Assistant Examiner	Total Claims <input type="checkbox"/> Print Claim for O.G. <input type="checkbox"/>
<b>ISSUE FEE</b>		Primary Examiner	<b>DRAWING</b>
Amount Due	Date Paid		Sheets Drwg. <input type="checkbox"/> Figs. Drwg. <input type="checkbox"/> Print Fig. <input type="checkbox"/>
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	<b>Application Examiner</b>
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